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IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): George Michael Hey

Serial No.: 10/032,194

Examiner: Not yet assigned

Filing Date: December 20, 2001

Group Art Unit: 2661

Title: APPARATUS AND METHOD FOR RATE ADAPTATION CONTROL

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ASSISTANT COMMISSIONER FOR PATENTS
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INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- ☒ under 37 CFR 1.97 (b), or
(Within three months of filing national application; or date of entry of International application; or before mailing date of first office action on the merits; whichever occurs last)
- ☐ under 37 CFR 1.97 (c) together with either a:
☐ Statement under 37 CFR 1.97 (e), or
☐ a \$240 fee under 37 CFR 1.17 (p), or
(After mailing of first Office Action, but prior to Notice of Allowance or Final Office Action)
- ☐ under 37 CFR 1.97 (d) together with either:
☐ Statement under 37 CFR 1.97 (e),
☐ Applicant hereby petitions for the consideration of the accompanying information disclosure statement (37 C.F.R. §1.97(d)(ii).
☐ a \$130.00 petition fee set forth in 37 CFR 1.17 (i)(1).
(Filed after final action or notice of allowance, whichever occurs first, but before payment of the

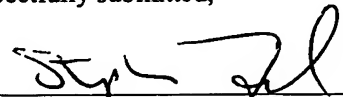
issue fee)

☒ Applicant(s) submit herewith Form PTO 1449- Information Disclosure Citation together with copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

It is requested that the information disclosed herein be made of record in this application.

Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.

Respectfully submitted,


Stephen S. Ford
Reg. No. 35,139

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Atty. Docket No.: 2705-203
Applicant: Hey
Filing Date: December 20, 2001

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Group: 2661

PATENT APPLICATION

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INFORMATION DISCLOSURE CITATION
FORM PTO-1449 (Modified)

U.S. PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Issue</u> <u>Date</u>	<u>Name</u>	<u>Class</u>	<u>Sub-</u> <u>Class</u>
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FOREIGN PATENT DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>Document</u> <u>Number</u>	<u>Publication</u> <u>Date</u>	<u>Country</u>	<u>Name</u>
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OTHER DOCUMENTS

<u>Exam</u> <u>Init</u>	<u>Ref</u>	<u>(Including Author, Title, Date, Pertinent Pages, Etc.)</u>
_____	_____	Preliminary Data Sheet, Microelectronics Group, Lucent Technologies Bell Labs Innovations, "TTSI4K32T 4096-Channel, 32-Highway Time Slot Interchanger", February 1999, 4 pages.
_____	_____	Xilinx Inc. Product Specification, Logicare Asynchronous FIFO V3.0, November 3, 2000, pages 1-12.

Examiner: _____

Date Considered: _____